

### REMARKS

This Amendment After Final Rejection is submitted in response to the outstanding Advisory Action, dated March 20, 2007. The present application was filed on February 26, 2004 with claims 1 through 20. Claims 1, 6-8, 12-14, and 19-20 were cancelled in the Amendment After Final Rejection dated March 5, 2007. Claims 2-5, 9-11, and 15-18 are presently pending in the above-identified patent application prior to entry of the present amendment. Claims 3, 9, and 16 are proposed to be cancelled, without prejudice, herein.

This amendment is submitted pursuant to 37 CFR §1.116 and should be entered. The Amendment places all of the pending claims, i.e., claims 2-5, 9-11, and 15-18, in a form that is believed allowable, and, in any event, in a better form for appeal. It is believed that examination of the pending claims as amended, which are consistent with the previous record herein, will not place any substantial burden on the Examiner. In any case, a Request for Continued Examination is being submitted herewith.

In the final Office Action dated December 4, 2006, the Examiner rejected claims 1-20 under 35 U.S.C. §102(e) as being anticipated by Salmonsens et al. (United States Patent Publication Number 2004/0054689)

#### Independent Claims 5, 11 and 18

Independent claims 5, 11, and 18 were rejected under 35 U.S.C. §102(e) as being anticipated by Salmonsens et al.

Independent claims 5, 11, and 18 have been amended to incorporate the basic limitation of claim 3. In rejecting claim 3, the Examiner asserted that Salmonsens discloses a controller wherein said processor provides processing capacity for use by said peripheral device (page 3, paragraph [0033]). In the text cited by the Examiner, Salmonsens teaches that

the processor 214 is capable of executing various processes, methods, or programs *to transfer information between the network 206 and the device 202 or bus 204 and to perform a wide variety of other functions*. The emulator interface 200 may include other optional functional blocks such as a volatile memory 216 and nonvolatile memory 218 that may be coupled to the interface controller 210. The volatile memory 216, for example synchronous dynamic random access memory (SDRAM), may be used to store information such as temporary control information, transferring data in various formats, and others. The nonvolatile memory 218, for example a bootstrap read-only memory (ROM), may be used to store executable function code such as a bootstrap load program and other operational functions, and operating parameters.

(Paragraph [0033]; emphasis added.)

Contrary to the Examiner's assertion, however, Applicants could find no disclosure or suggestion in Salmonsens that the processor provides *processing capacity* for use by the peripheral device. As would be apparent to a person of ordinary skill in the art, *processing capacity* for use by the peripheral device includes capacity for processing *typically performed by a processor in the peripheral device*

Thus, Salmonsens et al. do not disclose or suggest wherein said processor provides processing capacity for use by said peripheral device, as required by each independent claim 5, 11, and 18, as amended.

Applicants respectfully request the withdrawal of the rejection of independent claims 5, 11 and 18, as amended.

Dependent Claims

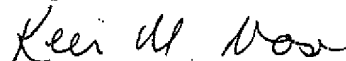
Claims 2-4, 9-10 and 15-17 are dependent on independent claims 5, 11, and 18, and are therefore patentably distinguished over Salmonsens et al. because of their dependency from amended independent claims 5, 11, and 18 for the reasons set forth above, as well as other elements these claims add in combination to their base claim. Claims 3, 9, and 16 are proposed to be cancelled herein.

All of the pending claims following entry of the amendments, i.e., claims 2, 4, 5, 10, 11, 15, 17, and 18, are in condition for allowance and such favorable action is earnestly solicited.

If any outstanding issues remain, or if the Examiner has any further suggestions for expediting allowance of this application, the Examiner is invited to contact the undersigned at the telephone number indicated below.

The Examiner's attention to this matter is appreciated.

Respectfully submitted,



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